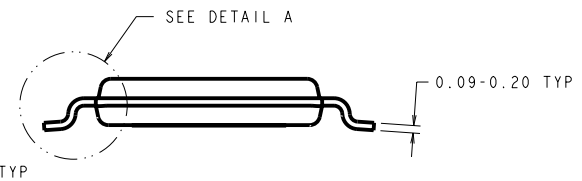
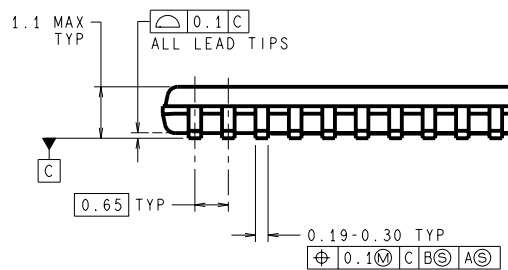
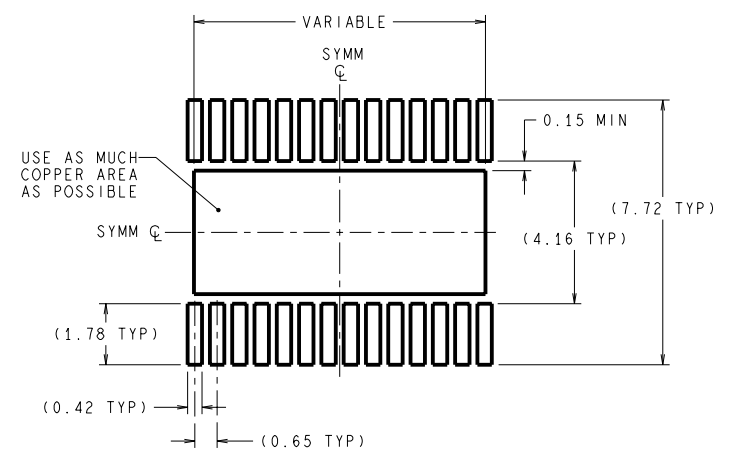
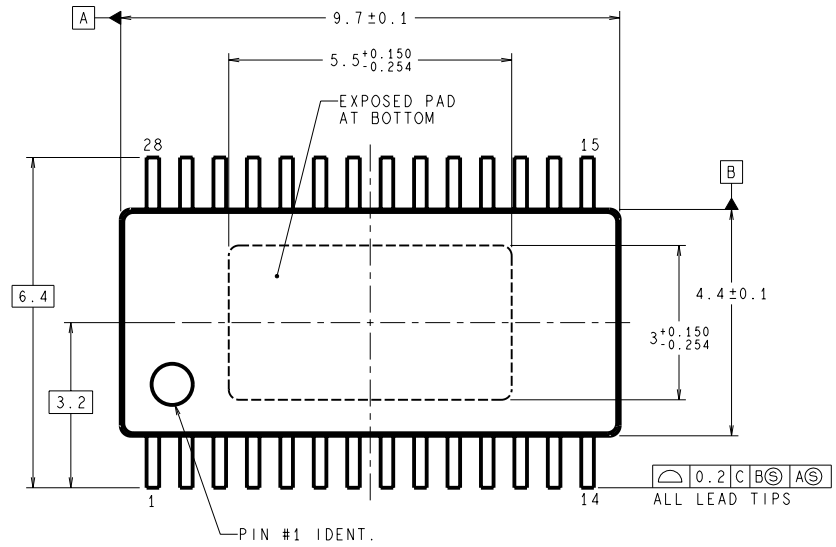
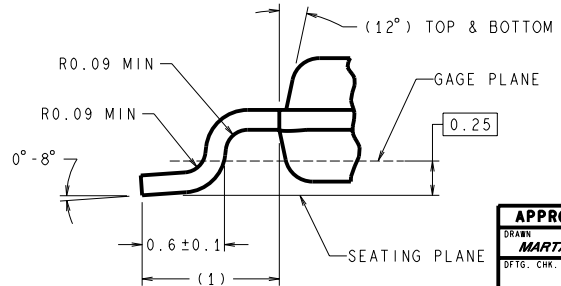


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	11918	01/26/1998	MS/JB
B	EXP. PAD TOL'S WERE ±0.15	12098	09/16/1998	MS/



DIMENSIONS ARE IN MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED
 1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION AET, DATED 10/97.



DETAIL A
TYPICAL, SCALE: 40X

APPROVALS	DATE	National Semiconductor		
DRWN: MARTA SUCHY	01/26/1998	2900 Semiconductor dr., Santa Clara, CA 95052-8090		
DTG: CHK.		MOLDED TSSOP, JEDEC, EXP PAD, 9.7x4.4x0.9mm BODY, 28 LD, 0.65mm PITCH		
ENGR: CHK.		SCALE: N/A	SIZE: C	DRAWING NUMBER: (SC)MKT-MXA28A
PROJECTION: 1st Angle		DO NOT SCALE DRAWING	REV: B	SHEET 1 of 1